

Abstract of the Disclosure

A power supply for a capacitive-resistive load includes plural paralleled phase correcting modules together with current sharing controllers for tending to equalize their currents. Each module is provided with a diode, poled to prevent forward current from flowing in the return current path, for aiding in equalizing module currents. Surge currents are reduced by a single saturable reactor coupled to the combined outputs of current sharing controllers, thereby avoiding the need for soft-start in each controller. A precharging path extends from a source of pulsating direct voltage to the load, for precharging the load capacitance at turn-on.